

CALL FOR TWO OPEN POSITIONS

The Solid State Electronics Section (SEES) of the Electrical Engineering Department of Cinvestav invites scientists interested in participating in the selection process to occupy two positions as full-time professors, based on the following:

Bases:

- Specialty Area: Solid State Electronics and/or related areas.
- Academic Degree: PhD or equivalent
- Being under 38 years of age (men) or 40 years of age (women)
- A minimum of three years in teaching and research in the subject area of their expertise, including postdoctoral experience
- Scientific-technological research papers published in JCR-Web of Science journals, which certify their independence and competence in the area of their specialty
- A written project in one of the following lines of research: Nanoelectronics, Synthesis and Characterization of Materials for Electronics, Solar Cells, Semiconductor Devices and Design of Integrated Circuits (VLSI) or similar.

To participate, interested persons must send in electronic format (all documents must be in a single pdf file) to Ms. Mónica Davar, (mdavar@cinvestav.mx) the following:

- Application letter, requesting to be considered in this call and stating the reasons for being interested in one of the positions.
- Updated Curriculum Vitae.
- Curricular concentrate (one page).
- A simple copy of the PhD Certificate (The selected candidates have to present the original or a certified copy)
- Project proposal to be developed.
- Two letters of recommendation

CALENDAR

Applications received	From August 1 to September 29.
Notification to preselected candidates	October 6
Oral presentation of the projects	October 16-to 27
Final Selection	6-10 de noviembre
Preliminary notifications and application for approval to Cinvestav General Director	13 de noviembre

It is important to note that this procedure does not constitute an obligation to hire any of the candidates

KIND REGARDS

The Evaluation Committee